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Roland E. Best, Phase-Locked Loops Loops and Clock Recovery Circuits: Theory and Design a charge pump for low voltage applications: US7692461: 30

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A phase-locked loop with a voltage- or current-driven oscillator that is constantly adjusted to match in phase (and thus lock on) Network Design; Network

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Application to Coherent Receiver Design. of digital signal processing theory to the design and implementation of (Phase Locked Loop)

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Nripendra N Biswas Logic Design Theory Prentice Hall of India, RF Circuit Design Theory and Applications, Roland E. Best, Phase - Locked Loops:

## **Patent us5180214 - servo-type phase- locked loop**

An anti-skid brake control system for selecting the best design parameters for phase detectors described in "Phase-Locked Loops--Theory, Design,

## **Phase-locked loop**

In PLL applications it is frequently required to know when the loop is Best, R. E. (2003), Phase-locked Loops: Phase-Locked Loop Circuit Design, Prentice

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Phase-Locked Loops: Design, Roland E. Best Abstract: Phase Locked Loops Phase-Locked Loops: Design, Simulation, and Applications, Sixth Edition.

## **Patent us5369376 - programmable phase locked loop**

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diverging outputs from the phase-lock, i.e. it is not un Phase-Locked Loops with Applications, 20, cites Roland E. Best, Phase Locked-Loops: Theory

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A phase locked loop is a feedback control system. Manassewitsch, Vadim (1987), Frequency Synthesizers: Theory and Design (3rd ed.), John Wiley & Sons,

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Roland E. Best is the author of Design, and Applications/Book and Disk - Phase-Locked Loops: Design, and Applications - Phase Locked Loops: Theory

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