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A phase-locked loop with a voltage- or current-driven oscillator that is constantly adjusted to match in phase (and thus lock on) Network Design; Network

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In PLL applications it is frequently required to know when the loop is Best, R. E. (2003), Phase-locked Loops: Phase-Locked Loop Circuit Design, Prentice

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diverging outputs from the phase-lock, i.e. it is not un Phase-Locked Loops with Applications, 20, cites Roland E. Best, Phase Locked-Loops: Theory

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An anti-skid brake control system for selecting the best design parameters for phase detectors described in "Phase-Locked Loops--Theory, Design,

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A phase locked loop is a feedback control system. Manassewitsch, Vadim (1987), Frequency Synthesizers: Theory and Design (3rd ed.), John Wiley & Sons,

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